



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the Application of:

William H. Robertson et al.

Serial No.: 10/600,999

Filed: June 20, 2003

For: METHOD AND SYSTEM FOR
FACILITATING ELECTRONIC CIRCUIT AND
CHIP DESIGN USING REMOTELY LOCATED
RESOURCES

)
) Group Art Unit: Not Yet Assigned

)
) Examiner: Not Yet Assigned

PRELIMINARY AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Prior to examination of this application on the merits, please amend the application as follows.
This amendment has been prepared in accordance with the new amendment practice in the revised 37
C.F.R. § 1.121.

09/24/2003 EAREGAY1 00000061 502518 10600999

01 FC:1001 750.00 DA
02 FC:1202 270.00 DA